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	L26	L25 same 16	21
	L25	11 same (\$5processor or cpu) near2 (core or module or device or module or block)	619
	L24	11 same (\$5processor or cpu) near2 (core or module or device or module)	454
	L23	cpu core same graphics core	0
	L22	cpu cache same graphics cache	1
	L21	cpu cache and graphics cache and memory	2
	L20	cpu cache same graphics cache same memory	1
	L19	6094203.pn.	2
	L18	L17 not L10	19
	L17	L12 same cache	24
	L16	L15 and supersampl\$3	3
	L15	L14 not L10	106
	L14	L13 and L1	130
	L13	L7 and (chip or die)	836
	L12	L9 same (chip or die)	72
	L11	L10 not intel	8
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	L9	L6 same L5	197
	L8	L7 and L1	156
	L7	L6 and L5	1098
	L6	(cpu or \$5processor or proces\$4 or accelerator or engine or device or core) near3 graphics	48109
	L5	(cpu or \$5processor) near3 core	10645
	L4	L2 and (cpu or \$5processor or proces\$4 or accelerator or engine or device or core)near3 graphics	10
	L3	L2 and (cpu or \$5processor or proces\$4)	55
	L2	L1 same graphic\$3 near2 (image or data)	55
	L1	(unif\$5 or shar\$3) near3 cache	5532

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cache <paragraph> graphics processor

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Results Key:

JNL = Journal or Magazine CNF = Conference STD = Standard

1 Pixel processing in a memory controller

Donovan, W.; Sabella, P.; Kabir, I.; Hsieh, M.M.;

Computer Graphics and Applications, IEEE , Volume: 15 , Issue: 1 , Jan. 1995 Pages:51 - 61

[Abstract] [PDF Full-Text (860 KB)] IEEE JNL

2 Efficient transmission of triangle meshes to graphics processors

Chou, P.H.; Meng, T.H.;

Signal Processing Systems, 2000. SiPS 2000. 2000 IEEE Workshop on , 11-13 2000

Pages:275 - 284

[Abstract] [PDF Full-Text (468 KB)] IEEE CNI

3 A real-time video-image mapping using polygon rendering technique *Ikedo, T.;*

Multimedia Computing and Systems '97. Proceedings., IEEE International Conference on , 3-6 June 1997

Pages:127 - 134

[Abstract] [PDF Full-Text (724 KB)] IEEE CNI

4 A low power and high performance core for planar object overlaying Budawy, W.; Bayoumi, M.;

Electronics, Circuits and Systems, 1999. Proceedings of ICECS '99. The 6th IE International Conference on , Volume: 2 , 5-8 Sept. 1999 Pages:621 - 624 vol.2

[Abstract] [PDF Full-Text (348 KB)] IEEE CNF

File 348: EUROPEAN PATENTS 1978-2004/Oct W05 (c) 2004 European Patent Office File 349:PCT FULLTEXT 1979-2002/UB=20041104,UT=20041028 (c) 2004 WIPO/Univentio Set Items Description S1 74219 CPU OR CENTRAL() PROCESS?() UNIT?? S2 121685 GRAPHIC? S3 2020 S2()PROCESSOR? OR GPU OR GRAPHIC?()PROCESS?()UNIT?? S4 279990 TEXT OR WORDS IMAG? OR PICTURE? OR PHOTO OR PHOTOGRAPH? S5 555432 S6 33182 (S4 OR NONGRAPHIC? OR NON-GRAPHIC?) (10N) (S2 OR S5) **S7** 114465 (ONE OR UNIFIED OR UNITED OR SHAR??? OR SINGLE) (3N) (BUFFER-?? OR CACHE OR MEMORY OR STORAGE OR STORING OR STORED) S8 AU=DESORMEAUX D? OR DESORMEAUX, D? HP OR HEWLETT() PACKARD S9 42704 AU=(TSAI, H? OR MAIYURAN, S? OR WANG, C? OR TSAI H? OR MAI-S10 741 YURAN S? OR WANG C?) 143903 IC=(G06F? OR G06T?) S11 S12 S9(10N)S7 20 S13 0 S12 (10N) S4 (7N) S5 S14 8 S12 AND S11 S15 5 S14 NOT PY=>1999 64 S10 AND S7 S16 0 S16(10N)S4(7N)S5 S17 0 S16(S)S4(S)S5 S18 0 S16(S)S6 S19 S20 16 S16 AND S11 S21 4 S20 NOT PY=>1998 32 S22 S1(10N)S3(10N)S7

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S24 NOT PY=>1998

S25 NOT (S14 OR S20)

S22 AND S11

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File 587: Jane's Defense&Aerospace 2004/Oct W5
         (c) 2004 Jane's Information Group
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S2
      2112477
                GRAPHIC?
                S2()PROCESSOR? OR GPU OR GRAPHIC?()PROCESS?()UNIT??
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S10
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             YURAN S? OR WANG C?)
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S11
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                S4(5N)S5(10N)S7
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          184
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                S21(S)S2
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S25
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RD S25 (unique items)

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S26